

#24/E

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| | | | |
|-------------------|-------------------|------------------|----------------------------|
| To: | Examiner Jung Hur | From: | Chad J. Billings |
| Fax: | (703) 746-9215 | Date: | June 25, 2003 |
| Phone: | | Pages: | 13 (including cover sheet) |
| Your Ref.: | 09/899,093 | Our Ref.: | 3672-0121P |
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Comments:

#24/E

VIA FACSIMILE**PATENT
3672-0121P****IN THE U.S. PATENT AND TRADEMARK OFFICE**

Applicant: THOMPSON, et al. Conf.: 2736
 Appl. No.: 09/899,093 Group: 2824
 Filed: July 6, 2001 Examiner: Jung H. HUR
 For: ADDRESSING OF MEMORY MATRIX

LARGE ENTITY TRANSMITTAL FORM

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

June 25, 2003

Sir:

Transmitted herewith is a Supplemental Amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
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| TOTAL | 18 | - | 20 | = | 0 | \$ 18 | \$0.00 |
| INDEPENDENT | 2 | - | 3 | = | 0 | \$ 84 | \$0.00 |
| <input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM | | | | | | \$280 | \$0.00 |
| | | | | | | TOTAL | \$0.00 |

Appl. No. 09/899,093

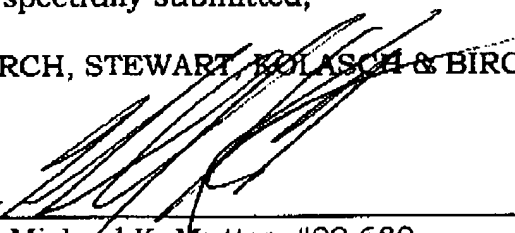
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Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By


Michael K. Mutter, #29,680MKM/CJB:cb
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Attachment(s)

[Rev. 04/30/03]

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PATENT
3672-0121P

VIA FACSIMILE

IN THE U.S. PATENT AND TRADEMARK OFFICE

| | | | |
|------------|-----------------------------|-----------|--------------|
| Applicant: | THOMPSON et al. | Conf.: | 2736 |
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| Filed: | July 6, 2001 | Examiner: | Jung, H. HUR |
| For: | ADDRESSING OF MEMORY MATRIX | | |

SUPPLEMENTAL AMENDMENT

Assistant Commissioner for Patents
And Trademarks
Washington, D.C. 20231

June 25, 2003

Sir:

This Response is a supplement to the Response filed on May 12, 2003.
The following amendments and remarks are respectfully submitted in
connection with the above-identified application.

IN THE SPECIFICATION

Please replace the paragraph starting at line 7 on page 27 of the
substitute specification with the following:

ts : Word line pulled to Vs (fig. 4) or zero (fig. 5) –

set/reset

~~capacitors~~ cells